

**Amendments to the Claims:**

The following listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Currently Amended) An electronic circuit, comprising:
  - a first transistor that has a first gate, a first drain and a first source,
  - a conduction state of the first transistor being set in accordance with a current signal supplied to a capacitor during a first period and a voltage signal supplied to the capacitor during a second period,  
a controller that controls a current outputting circuit and a voltage outputting circuit based on a desired power consumption level,  
the current outputting circuit outputting the current signal supplied to the capacitor,  
the voltage outputting circuit outputting the voltage signal supplied to the capacitor,  
~~a length of a third period in which the conduction state of the first transistor is set in accordance with the voltage signal being changeable,~~  
a first current as the current signal flowing through the first transistor during at least a part of the first period, and  
an amount of charge held in the capacitor being reset to a predetermined state when a second transistor is turned on.
2. (Currently Amended) The electronic circuit according to Claim 1, further comprising:
  - a third transistor, and
  - the capacitor,

the current signal and the voltage signal being supplied to the capacitor through the third ~~transistor~~transistor,

a controller that controls the first circuit and the second circuit based on a desired power consumption level.

3. (Previously Presented) The electronic circuit according to Claim 1, further comprising:

a fourth transistor that controls an electrical connection between the first gate and the first drain.

4. (Previously Presented) The electronic circuit according to Claim 1, further comprising:

a fifth transistor,

a second current whose level corresponds to the conduction state of the first transistor set in accordance with the current signal and the voltage signal,

the fifth transistor controlling a timing to start or stop a supply of the second current to an electronic element.

5. (Previously Presented) The electronic circuit according to Claim 1, no current flowing through the first transistor during the third period.

6. (Currently Amended) An electro-optical device, comprising:

a plurality of scanning lines;

a plurality of data lines;

a plurality of unit circuits;

a first circuit that outputs a current signal that is accumulated in a capacitor included in each of the plurality of unit circuits;

a second circuit that outputs a voltage signal that is accumulated in a capacitor included in each of the plurality of unit circuits, the second circuit being configured such that

a length of a third period in which a conduction state of a first transistor is set in accordance with the voltage signal is changeable, and

a second transistor, and

an amount of charge held in the capacitor being reset to a predetermined state when the second transistor is turned on.

a controller that controls the first circuit and the second circuit based on a desired power consumption level,

no current flowing through a transistor included in one unit circuit of the plurality of unit circuits during a second period in which the voltage signal is supplied to the one unit circuit.

7. (Previously Presented) The electro-optical device according to Claim 6, the current signal and voltage signal being supplied to each of the plurality of unit circuits through one data line of the plurality of data lines.

8. (Previously Presented) The electro-optical device according to Claim 6, the plurality of data lines including first data lines and a plurality of second data lines,

the current signal being supplied to each of the plurality of unit circuits through one first data line of the plurality of first data lines, and the voltage signal being supplied to each of the plurality of unit circuits through one second data line of the plurality of second data lines.

9-12. (Canceled)

13. (Previously Presented) The electro-optical device according to Claim 22, the electro-optical element being an EL element.

14. (Previously Presented) The electro-optical device according to Claim 13,

the EL element including a light-emitting layer that is composed of an organic material.

15-19. (Cancelled)

20. (Previously Presented) An electronic apparatus, comprising:

the electro-optical device according to Claim 6.

21. (Previously Presented) The electronic circuit according to Claim 1,

the current signal being a multi-valued data current, and

the voltage signal being a binary data voltage.

22. (Previously Presented) The electro-optical device according to Claim 6,

each of the plurality of unit circuits including an electro-optical element.

23. (Currently Amended) An electronic circuit, comprising:

a capacitor that accumulates a current signal that is received by the electronic circuit during a first period, the capacitor accumulating a voltage signal that is received by the electronic circuit during a second period;

a first transistor whose conduction state is set in accordance with an amount of charge accumulated in the capacitor stored during a period selected from the first period and the second period, the first transistor having a first gate, a first drain and a first source, the first transistor supplying a current whose level is determined in accordance with the conduction state to an electronic element,

a length of a third period in which no current flows through the first transistor is changeable; and changeable

a second transistor, and transistor,

an amount of charge held in the capacitor being reset to a predetermined state when the second transistor is turned on, turned on, and

a controller that controls a current outputting circuit and a voltage outputting circuit based on a desired power consumption level,

the current outputting circuit outputting the current signal accumulated by the capacitor,

the voltage outputting circuit outputting the voltage signal accumulated by the capacitor.

24. (Currently Amended) An electronic circuit, comprising:

a capacitor that accumulates a current signal that is received by the electronic circuit in a first mode, the capacitor accumulating a voltage signal that is received by the electronic circuit in a second mode;

a first transistor whose conduction state is set in accordance with an amount of charge accumulated in the capacitor stored during a mode selected from the first mode and the second mode, the first transistor having a first gate, a first drain and a first source, the first transistor supplying a current whose level is determined in accordance with the conduction state to an electronic element,

a length of a period in which no current flows through the first transistor is changeable; and

a second transistor, and~~transistor~~

an amount of charge held in the capacitor being reset to a predetermined state when the second transistor is ~~turned on~~turned on,

a controller that controls a current outputting circuit and a voltage outputting circuit based on a desired power consumption level,

the current outputting circuit outputting the current signal accumulated by the capacitor,

the voltage outputting circuit outputting the voltage signal accumulated by the capacitor.

25. (Previously Presented) The electronic circuit according to Claim 23, the current signal corresponding to analog data current, and the voltage signal corresponding to digital data voltage.
26. (Previously Presented) The electronic circuit according to Claim 24, the current signal corresponding to analog data current, and the voltage signal corresponding to digital data voltage.
27. (Previously Presented) The electronic circuit according to Claim 24, a power consumption in the second mode being lower than a power consumption in the first mode.
28. (Previously Presented) The electronic circuit according to Claim 23, further comprising:
  - a third transistor,
  - the current signal and the voltage signal being supplied to the capacitor through the third transistor.
29. (Previously Presented) The electronic circuit according to Claim 23, further comprising:
  - a fourth transistor that controls an electrical connection between the first gate and the first drain.
30. (Previously Presented) The electronic circuit according to Claim 1, further comprising:
  - an electronic element,
  - a second current whose current level corresponds to the conduction state of the first transistor being supplied to the electronic element.

31. (Previously Presented) The electro-optical device according to Claim 34,  
a potential of the first electrode being set at a constant during at least a part of  
a first period in which the current signal is supplied to the capacitor, and  
the potential of the first electrode being set at the constant during at least a part  
of a second period in which the voltage signal is supplied to the capacitor.

32. (Previously Presented) The electronic circuit according to Claim 1,  
the current signal corresponding to analog data current, and  
the voltage signal corresponding to digital data voltage.

33. (Previously Presented) The electro-optical device according to Claim 6,  
the current signal corresponding to analog data current, and  
the voltage signal corresponding to digital data voltage.

34. (Previously Presented) The electro-optical device according to Claim 6,  
further comprising:

a first electrode that is disposed opposite to a plurality of second electrodes,  
each of which is included in one electro-optical element of a plurality of electro-optical  
elements included in the plurality of unit circuits.

35. (New) The electronic circuit according to Claim 1,  
the conduction state of the first transistor being set in accordance with a  
current signal supplied to the capacitor during the first period, the first period being a period  
during which a high luminance is desired, and  
the conduction state of the first transistor being set in accordance with a  
voltage signal supplied to the capacitor during a second period, the second period being a  
period during which a lower luminance is desired.

36. (New) The electronic circuit according to Claim 6,

the conduction state of the first transistor being set in accordance with a current signal supplied to the capacitor during the first period, the first period being a period during which a high luminance is desired, and

the conduction state of the first transistor being set in accordance with a voltage signal supplied to the capacitor during a second period, the second period being a period during which a lower luminance is desired.

37. (New) The electronic circuit according to Claim 23,

the conduction state of the first transistor being set in accordance with a current signal supplied to the capacitor during the first period, the first period being a period during which a high luminance is desired, and

the conduction state of the first transistor being set in accordance with a voltage signal supplied to the capacitor during a second period, the second period being a period during which a lower luminance is desired.

38. (New) The electronic circuit according to Claim 24,

the conduction state of the first transistor being set in accordance with a current signal supplied to the capacitor during the first period, the first period being a period during which a high luminance is desired, and

the conduction state of the first transistor being set in accordance with a voltage signal supplied to the capacitor during a second period, the second period being a period during which a lower luminance is desired.